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### [PDF] Surpassing the TLB Performance of Superpages with Less Operating ...

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we show that superpages improve TLB performance only after using a superpage TLB with the same number of entries. Also the increased area can translate into ...

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### Microprocessors and Microsystems : Aggressive superpage Support ...

Partial-subblock TLB with a superpage-aware paging system ... In a superpage TLB, a superpage can be mapped only to contiguous and aligned physical pages ...

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### Increasing TLB reach with multiple pages size subblocks ...

As a result, TLB reach increases by the superpage factor ... employs as its TLB the primitive superpage TLB without modification. Therefore, the superpage ...

<ieeexplore.ieee.org/iel5/7810/21465/00995143.pdf?arnumber=995143> - Similar pages

### Surpassing the TLB Performance of Superpages with Less Operating ...

4.5 times larger than a superpage. TLB with the same number of entries. Also the increased ... Single-Page-Size TLB, E.g., 110100. 4K/16K Superpage TLB ...

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### [PPT] Practical, transparent operating system support for superpages

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A superpage TLB, base page entry (size=1), superpage entry (size=4), physical memory, virtual memory, virtual address, TLB, physical address ...

<www.cs.rice.edu/~jnavarro/papers/osdi02.ppt> - Similar pages

### [PDF] Lecture 14: Practical, transparent operating system support for ...

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TLB size does not grow at same pace. • Processors now provide superpages ... A superpage TLB, base page entry (size=1), physical memory, virtual memory ...

[www.cs.toronto.edu/~demke/469F.06/Lectures/Lecture14\\_4up.pdf](www.cs.toronto.edu/~demke/469F.06/Lectures/Lecture14_4up.pdf) - Similar pages

### Superpages allow each entry in the TLB to map a large physical ...

INCREASES TLB coverage; REDUCES TLB misses; Challenges of superpages: (1) superpage allocation & promotion (2) fragmentation control TLB coverage: amount of ...

[www.stanford.edu/~stinson/cs240/cs240\\_1/refs/superpages.txt](www.stanford.edu/~stinson/cs240/cs240_1/refs/superpages.txt) - 20k -

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### [PPT] Superpages

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Processors now provide superpages. one TLB entry can map a large region. OSs have been slow to harness them. no transparent superpage support for apps ...

<www.cs.binghamton.edu/~mlewis/CS552/slides/cs552-superpages.ppt> - Similar pages

**Variable Radix Page Table: A Page Table for Modern Architectures**  
superpage TLB allows pages of different sizes to be used simultaneously, even ... current  
processors provide superpage TLBs to extend TLB coverage rather ...  
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### SuperPages - IA64wiki

Superpages are large virtual pages used to increase the memory mapped by each page  
table entry. Because the resident TLB entries then map a larger area, ...  
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